

OVERVIEW OF VECTOR NETWORK ANALYZERS

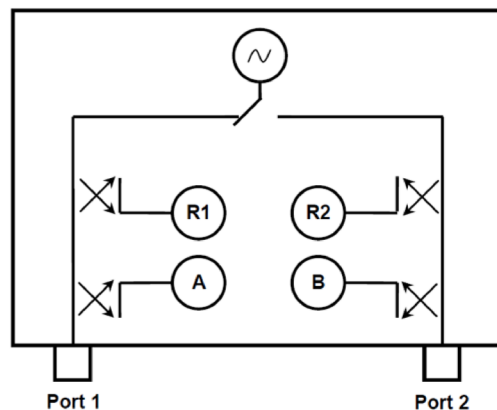
VNAs measure the characteristics of a Device Under Test (DUT) by evaluating incident and reflected RF signals.

An incident signal leaves Port 1. Some of the incident signal is reflected by the DUT. Some passes through the DUT to Port 2.



S-parameters express the ratio of these incident and reflected waves.

Vector Network Analyzer Block Diagram



- The RF source switches back and forth between Ports 1 and 2.
- Measurement bridges on each side separate incident from reflected waves.
- Incident and Reflected waves are digitized and ratioed to form S_{11} , S_{21} , S_{12} , and S_{22} S-parameters.

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